



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): NOVAKOVSKY,
Alexander et al. EXAMINER: Not yet assigned

SERIAL NO.: 10/720,672 GROUP ART UNIT: Not yet assigned

FILED: November 25, 2003 ATTORNEY DOCKET No.: P-5667-US

FOR: DEVICE, SYSTEM AND METHOD FOR VLSI DESIGN ANALYSIS

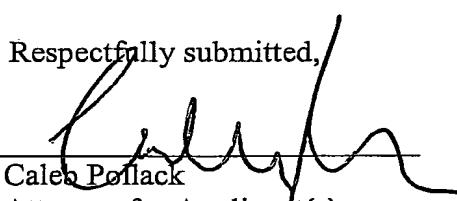
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Attn: Official Draftsperson

SUBMISSION OF FORMAL DRAWINGS

Sir:

Applicant(s) submit(s) two sets of formal drawings, each set consisting of 2 pages, in the above-identified Application. If there are any questions regarding these drawings, please call the undersigned attorney of record at (212) 632-3480.

Respectfully submitted,

Caleb Pollack
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Dated: January 20, 2004

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